

PATENT NUMBER and
ISSUE DATE

U.S. UTILITY Patent Application

APPL NUM	FILING DATE	CLASS	SUBCLASS	GAU	EXAMINER
10081546	02/21/2002	365	233	2818	MAI, SON

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**CONTINUING DATA VERIFIED:

NONE - file

** FOREIGN APPLICATIONS VERIFIED:

REPUBLIC OF KOREA 2001-24044 03/05/2001

REPUBLIC OF KOREA 2001-44065 07/21/2001

PG-PUB	DO NOT PUBLISH <input type="checkbox"/>	RESCIND <input type="checkbox"/>	
Foreign priority claimed 35 USC 119 conditions met		<input checked="" type="checkbox"/> yes <input type="checkbox"/> no	ATTORNEY DOCKET NO
Verified and Acknowledged Examiner's initials		<input checked="" type="checkbox"/> yes <input type="checkbox"/> no	8729-214 (SS-15661-US)
TITLE : Data input circuit and method for synchronous semiconductor memory device			
U.S. DEPT. OF COMM./PAT. & TM-PTO-436L (Rev. 12-94)			

NOTICE OF ALLOWANCE MAILED		CLAIMS ALLOWED	
		Total Claims	Print Claim for O.G.
Assistant Examiner		DRAWING	
		Sheets Drawn	Fig.s Drawn
			Print Fig.
Primary Examiner			
PREPARED FOR ISSUE		Application Examiner	
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